

## IN THE CLAIMS

Please amend the claims as follows:

1. (Currently amended) A method for selectively enabling operating modes of a device during a device initialization, wherein the operating modes comprise a privileged mode and a non-privileged mode, and the method comprising:

    determining during the device initialization whether the device is to operate either in the privileged mode or in both the privileged and non-privileged modes; and

    enabling the privileged mode if it is determined that the device is to operate only in the privileged mode; [[or]] and

    enabling both the privileged and the non-privileged modes if it is determined that the device is to operate in both the privileged and the non-privileged modes, wherein programs operating in the privileged mode have unlimited access to device memory and/or device functions and programs operating in the non-privileged mode have limited access to device memory and/or device functions.

2. (Original) The method of claim 1, wherein the step of determining comprises testing a flag.

3. (Original) The method of claim 1, wherein the step of enabling only the privileged mode comprises controlling one or more device memory management units to enable only the privileged mode.

4. (Original) The method of claim 1, wherein the step of enabling both the privileged mode and the non-privileged modes comprises controlling one or more device memory management units to enable both modes.

5. (Original) The method of claim 1, wherein the device is a wireless device.

6. (Previously presented) Apparatus for selectively enabling operating modes of a device during a device initialization, wherein the operating modes comprise a privileged mode and a non-privileged mode, and the apparatus comprising:

    a flag; and

selection logic that operates to read the flag to set the operating mode of the device, wherein if the flag is set the selection logic enables only the privileged mode, and if the flag is not set, the selection logic enables both the privileged and non-privileged modes, wherein programs operating in the privileged mode have unlimited access to device memory and/or device functions and programs operating in the non-privileged mode have limited access to device memory and/or device functions.

7. (Original) The apparatus of claim 6, further comprising a memory that stores the flag.

8. (Original) The apparatus of claim 6, further comprising one or more memory management units that are controlled by the selection logic to set the operating mode of the device.

9. (Original) The apparatus of claim 6, wherein the device is a wireless device.

10. (Currently amended) Apparatus for selectively enabling operating modes of a device during a device initialization, wherein the operating modes comprise a privileged mode and a non-privileged mode, and the apparatus comprising:

means for determining during the device initialization whether the device is to operate either in the privileged mode or in both the privileged and non-privileged modes; and

means for enabling only the privileged mode if it is determined that the device is to operate only in the privileged mode; [[or]] and

means for enabling both the privileged and the non-privileged modes if it is determined that the device is to operate in both the privileged and the non-privileged modes, wherein programs operating in the privileged mode have unlimited access to device memory and/or device functions and programs operating in the non-privileged mode have limited access to device memory and/or device functions.

11. (Original) The apparatus of claim 10, wherein the means for determining comprises means for testing a flag.

12. (Original) The apparatus of claim 10, wherein the means for enabling the only privileged mode comprises means for controlling one or more device memory management units to enable only the privileged mode.

13. (Original) The apparatus of claim 10, wherein the means for enabling both the privileged mode and the non-privileged modes comprises means for controlling one or more device memory management units to enable both modes.

14. (Original) The apparatus of claim 10, wherein the device is a wireless device.

15. (Currently amended) A computer-readable media comprising instructions, which when executed by a processor in a device, operate to selectively enable operating modes of a device during a device initialization, wherein the operating modes comprise a privileged mode and a non-privileged mode, and the computer-readable media comprising:

instructions for determining during the device initialization whether the device is to operate either in the privileged mode or in both the privileged and non-privileged modes; and

instructions for enabling only the privileged mode if it is determined that the device is to operate only in the privileged mode; [[or]] and

instructions for enabling both the privileged and the non-privileged modes if it is determined that the device is to operate in both the privileged and the non-privileged modes, wherein programs operating in the privileged mode have unlimited access to device memory and/or device functions and programs operating in the non-privileged mode have limited access to device memory and/or device functions.

16. (Original) The computer-readable media of claim 15, wherein the instructions for determining comprise instructions for testing a flag.

17. (Original) The computer-readable media of claim 15, wherein the instructions for enabling the only privileged mode comprise instructions for controlling one or more device memory management units to enable only the privileged mode.

18. (Original) The computer-readable media of claim 15, wherein the instructions for enabling both the privileged mode and the non-privileged modes comprise instructions for controlling one or more device memory management units to enable both modes.

19. (Original) The computer-readable media of claim 15, wherein the device is a wireless device.

20. (Cancel)

21. (Cancel)

22. (Cancel)

23. (Cancel)

24. (Previously presented) An apparatus, comprising:

a selectable one of a plurality of operating modes, the plurality of operating modes comprising at least a privileged operating mode and a combined privileged and non-privileged operating mode;

a memory comprising a flag having at least two settings, wherein a first setting of the at least two settings corresponds to the combined privileged and non-privileged operating mode and a second setting of the at least two settings corresponds to the privileged operating mode; and

selection logic communicatively coupled with the memory and operable to read the flag to set an operating mode of the apparatus, wherein the selection logic is operable to enable the combined privileged and non-privileged mode on the apparatus based on reading the first setting of the at least two settings and the selection logic is operable to enable only the privileged mode on the apparatus based on reading the second setting of the at least two settings, wherein programs operating in the privileged mode have unlimited access to device memory and/or device functions and programs operating in the non-privileged mode have limited access to device memory and/or device functions.

25. (Previously presented) The apparatus of claim 24, wherein the memory further comprises a code memory and a data memory, further comprising:

wherein the code memory is operable to store code;

a first memory management unit operable, under control of the selection logic, to partition the code memory into a privileged code region comprising privileged code and a non-privileged code region comprising non-privileged code;

wherein the data memory is operable to store data; and  
a second memory management unit operable, under control of the selection logic, to partition the data memory into a privileged data region comprising privileged data and a non-privileged data region comprising non-privileged data.

26. (Previously presented) The apparatus of claim 25, wherein the first memory management unit is operable to restrict operation of the non-privileged code to the non-privileged code region of the code memory, and wherein the second memory management unit is operable to restrict operation of the non-privileged code to the non-privileged data region of the data memory.